Lesson Plan

NameoftheFaculty : **Ms.Anshita**
Discipline : **B.Tech(ECE)**
Semester : **5thSemester**
Subject : **ComputerOrganization&Architecture (PCC-ECE-303G)**
LessonPlan Duration : **15 Weeks**
WorkLoadperweek(inhours) : **Lectures-03**

Week	**Theory**	
	Lecture Day	Topic(includingassignment/Test)
1.	1.	DataRepresentation,DataTypes,Complements
	2.	FixedpointRepresentation, FloatingpointRepresentation Conversion of Fractions
	3.	Graycodes,Decimalcodes,Alphanumeric codes
2.	4.	Errordetectioncodes
	5.	RegisterTransfer&Microoperations
	6.	RegisterTransferLanguage,RegisterTransfer
3.	7.	Busandmemorytransfer
	8.	Arithmeticmicrooperations,logicmicrooperations
	9.	Shiftmicrooperations,Arithmeticlogicshiftunit
4.	10.	BasicComputerOrganization& design
	11.	Instructioncodes,Computerregisters
	12.	Computerinstructions,Timingandcontrol
5.	13.	Instructioncycle,Memoryreferenceinstructions
	14.	Input-outputinstructions,CompletecomputerDescription
	15.	Designofbasiccomputer,Designofaccumulatorlogic
6.	16.	CPU,Generalregisterorganization,Stackorganization
	17.	Instructionformat
	18.	Addressingmodes
7.	19.	Assignment1
	20.	DatatransferandmanipulationProgramcontrol,
	21.	RISC,CISC
8.	22.	Introductiontoparallelprocessors,Amdahl'slaw
	23.	Pipelining,ArithmeticPipeline
	24.	InstructionPipeline
9.	25.	RISCPipeline,VectorProcessing,
	26.	Array Processors
	27.	SIMDArray Processors
10.	28.	PipelineHazards
	29.	1/0device interface,
	30.	1/0transfers-programcontrolled
11.	31.	InterruptdrivenandDMA

	32.	Privilegedandnon-privilegedinstructions
	33.	Assignment2
12.	34.	Softwareinterrupts
	35.	Memory Hierarchy
	36.	Mainmemory
13.	37.	Auxiliarymemory
	38.	Associativememory
	39.	cachememory,Associativemapping
14.	40.	Directmapping
	41.	Setassociativemapping
	42.	Writingintocache
15.	43.	Cacheinitialization
	44.	Virtualmemory
	45.	Revision